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A diagram showing a 10x3 grid of rectangles. The rectangles are arranged in 10 rows and 3 columns. The top row is labeled "10" on the left and "3" on the right. The second row is labeled "7" on the right. The third row is labeled "4" on the left. The fourth row is labeled "2" on the left. The fifth row is labeled "1" on the left. The sixth row is labeled "1" on the left. The seventh row is labeled "1" on the left. The eighth row is labeled "1" on the left. The ninth row is labeled "1" on the left. The tenth row is labeled "1" on the left. The rectangles are arranged in a way that they form a larger rectangle with a total width of 10 and a total height of 3.







Diagram illustrating a memory layout with various sized blocks (words) and pointers. The layout consists of the following blocks in sequence:

- Block 1: 3 words
- Block 2: 25 words
- Block 3: 10 words
- Block 4: 4 words (labeled "1")
- Block 5: 10 words (labeled "2")
- Block 6: 1 word
- Block 7: 25 words
- Block 8: 10 words
- Block 9: 4 words (labeled "3")
- Block 10: 25 words

The diagram consists of six horizontal bars, each made of small squares. The lengths of the bars are as follows:

- Bar 1: 3 squares
- Bar 2: 20 squares
- Bar 3: 15 squares
- Bar 4: 25 squares
- Bar 5: 25 squares
- Bar 6: 12 squares

Diagram illustrating a memory array structure. The array is organized into four horizontal sections, each containing 16 rows and 64 columns of cells. The total width of the array is 1024 bits, and the total height is 512 bits. The array is divided into four 512-bit sections by horizontal lines. The top section is labeled '1024' on the right. The bottom section is labeled '512' on the right.

