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[illegible][illegible][illegible]

Diagram illustrating a 16-bit bus system with four 4-bit registers. The top two registers are full of data. The bottom two registers show a sequence: the first has data, followed by a quote, then a register with data, an arrow, another register with data, another arrow, a third register with data, and a closing quote.

“III”

[illegible]

1. 
2. 
3. 
4. 
5. 

“ ”

[illegible]

Diagram illustrating a sequence of 32 boxes (likely representing data or memory) arranged in four rows. The first row contains 32 boxes. The second row contains 32 boxes. The third row contains 4 boxes, followed by a double quote, then 4 boxes, followed by another double quote. The fourth row contains 32 boxes. The fifth row contains 12 boxes.

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