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Diagram illustrating the instruction format (64 bits) and its fields:

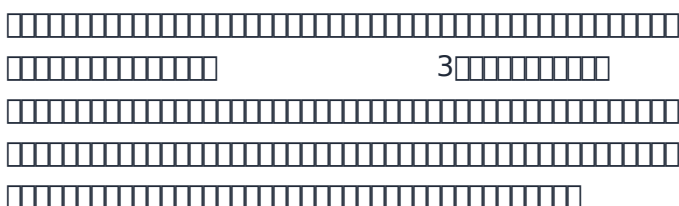
- Field 1: 8 bits (Opcode)
- Field 2: 16 bits (Register Index)
- Field 3: 8 bits (Register Index)
- Field 4: 16 bits (Register Index)
- Field 5: 8 bits (Register Index)

The instruction is shown in a sequence of five rows, with the first row containing the opcode and register indices, and the subsequent rows containing the register indices. The label "App" is at the bottom right.



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1.  App 
2. 
3. 
4. 
5. 



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