

[illegible]

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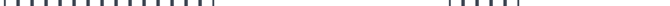
2026-10-19

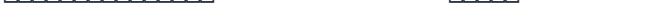


Four rows of empty boxes for writing, each row consisting of 20 small squares.

[illegible]





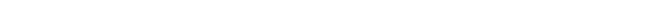




Diagram illustrating the instruction format (32 bits):

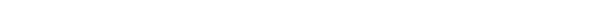
- Opcode (8 bits)
- Register number (8 bits) - App
- Register number (8 bits) - Rn
- Register number (8 bits) - Rd

[illegible]

1




 " 
 - 
 - 
 "


 "


 " 
 " 
 "

[illegible]

1. 
2. 
3. 
4. 
5. 

[illegible]

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[illegible][illegible]

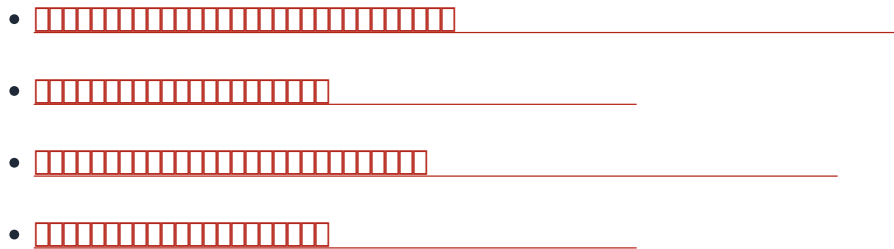
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-  _____
-  _____
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Updated 2026-10-19 14:48:15 UTC by CCSST[REDACTED]