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Diagram illustrating the alignment of a 16-bit instruction across four rows of memory cells:

- Row 1: 16 cells (0 to 15)
- Row 2: 8 cells (8 to 15)
- Row 3: 16 cells (0 to 15)
- Row 4: 16 cells (0 to 15)

The instruction is shown as a sequence of 16 bits, with the first 8 bits aligned with the start of the second row and the last 8 bits aligned with the start of the third row.

“ ” “ ”

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1

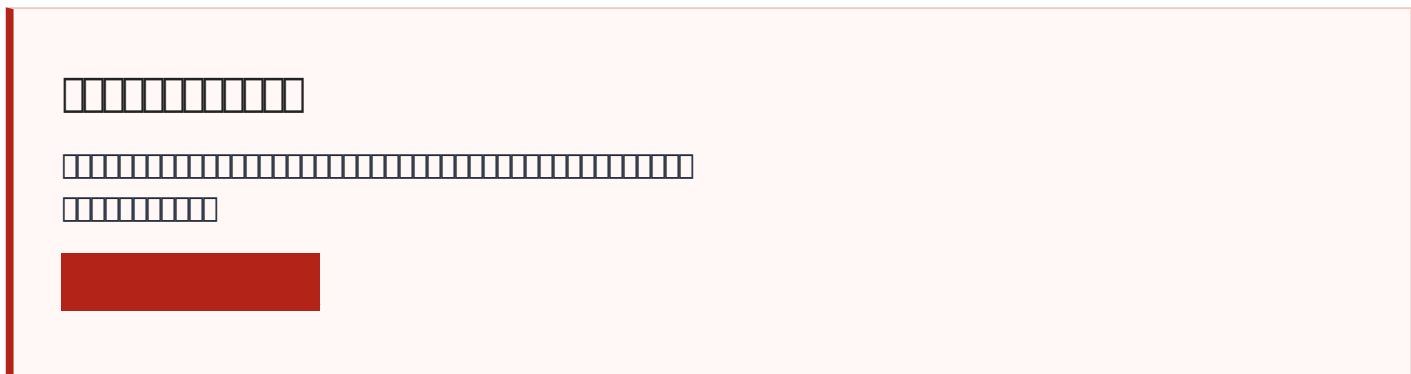
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1. 
2. 
3. 
4. 
5. 

[illegible]

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Updated 2026-10-10 16:08:17 UTC by CCSST