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The diagram illustrates the reduction of a 7-bit number to a 4-bit number. It shows a 7-bit number (represented by 7 vertical bars) being reduced to a 4-bit number (represented by 4 vertical bars) using a 3-bit mask (represented by 3 vertical bars). The reduction is shown as a sequence of steps: 7 bits, then 6 bits, then 5 bits, then 4 bits. The final 4-bit number is shown with a 3-bit mask applied to it.

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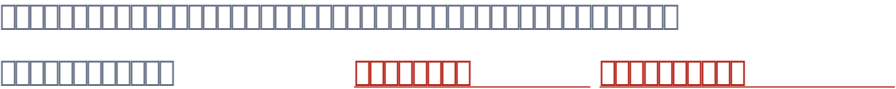
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