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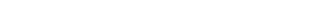
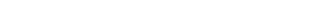
The diagram illustrates a 16-bit bus system. It features two 8-bit registers, each represented by a horizontal row of eight small squares. The top register is connected to a 16-bit bus, which is a long horizontal row of sixteen small squares. This bus is connected to a second 16-bit bus, which is also a long horizontal row of sixteen small squares. The second 16-bit bus is connected to a second 8-bit register. The entire system is enclosed in a rectangular frame.

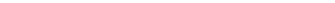


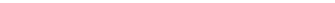
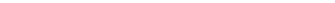


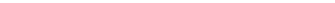


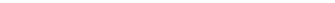
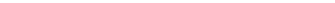
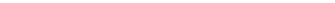
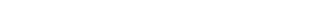






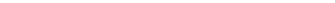
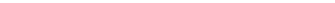





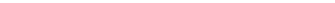
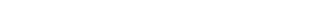


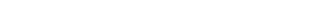
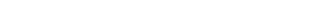


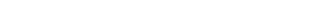


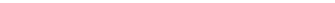


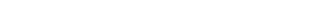
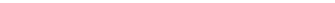
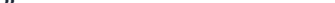



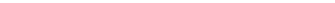
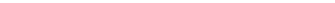



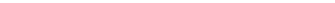
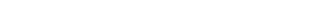



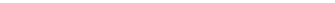


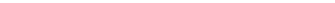




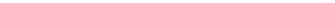
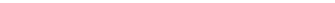










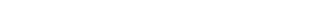
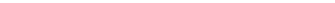
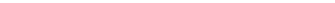












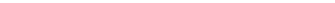




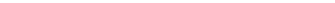
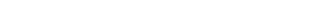
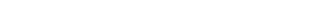


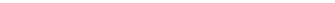
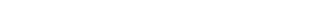
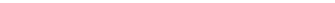


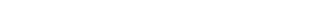













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Revision #171

Created 2026-08-18 10:20:16 UTC by CCSST[REDACTED]

Updated 2026-08-19 13:18:17 UTC by CCSST[REDACTED]